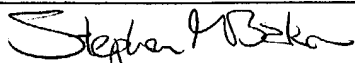


Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	unassigned
				Filing Date	September 28, 2001
				First Named Inventor	John D. Acton
				Group Art Unit	unassigned
				Examiner Name	unassigned
Sheet	2	of	2	Attorney Docket Number	P6196 US

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SH	F	HEGDE, ANUPAMA, "Error Detection and Correction with IDT49C466," <i>Integrated Device Technology, Inc., Appl. Note AN-94</i> , 1996, pages 131-141.	
SH	G	Supplement A.6-5, "Error Detection and Correction," <i>Logic and Computer Design Fundamentals</i> , 1997, 2000 Prentice-Hall, Inc., pages 1-5.	
SH	H	WICKMAN ET AL., "Cost Models for Large File Memory DRAMs with ECC and Bad Block Marking," <i>Department of Electrical and Computer Engineering, University of Alberta, Edmonton, AB T6G 2G7, CANADA</i> , pages 1-9.	

Examiner Signature		Date Considered	5/28/04
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(1449B/PTO Modified)

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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